



## FOR IMMEDIATE RELEASE

### Teradyne, Galaxy Semiconductor Partner to Deliver Part Average Testing Solutions

**North Reading and Waltham, Mass.** – July 7, 2009 – [Teradyne, Inc.](#) (NYSE:TER) and [Galaxy Semiconductor Solutions](#) have entered into a strategic alliance to jointly offer customers solutions for optimizing device quality and reliability by utilizing Part Average Testing and other Defects Per Million (DPM) reduction techniques.

Under the terms of the agreement, Teradyne's Test Assistance Group's (TAG) network of more than 500 applications engineers will offer applications engineering and integration services to customers who wish to deploy Galaxy's [PAT-MAN™](#) software in a production environment. Teradyne and Galaxy will jointly market and deliver the solution.

Galaxy's PAT-MAN is a comprehensive software solution for Parts Average Testing (PAT) and related DPM reduction techniques, which manages the entire PAT process from initial wafer lot characterization to final-test yield monitoring. PAT-MAN employs a closed-loop approach to PAT, which helps improve device quality while minimizing unnecessary yield loss. PAT-MAN also offers a unique, easy-to-use environment for fast setup and simulation of outlier rules, and an automated monitoring system for managing PAT-related yield in production.

"We are pleased to include Galaxy in our Third Party Solution Provider Program," said Roy Chorev, Teradyne's Semiconductor Test software product manager. "Galaxy's PAT-MAN software clearly enhances the value of our test systems for customers in the automotive electronics market and other high reliability sectors. With our Galaxy partnership, we can now help customers rapidly deploy PAT while optimizing their overall test process."

"A number of Teradyne's major customers have successfully deployed Part Average Testing using Galaxy PAT-MAN in conjunction with Teradyne FLEX™ and J750 test systems," said Bertrand Renaud, COO at Galaxy. "By partnering with Teradyne, we can now offer other customers a complete solution for PAT and other DPM reduction techniques leveraging the production-proven PAT-MAN software and the expertise of Teradyne's TAG organization."

#### **About Galaxy**

Galaxy Semiconductor Solutions is a leading provider of test data analysis, yield management and quality improvement solutions for the semiconductor industry. Galaxy products are used by over 2000 users

(more)

worldwide to help increase device yields, reduce defects and improve engineering productivity. Galaxy products are easy to learn and use, and offer a very short time-to-benefit. More information about Galaxy can be found at [www.galaxysemi.com](http://www.galaxysemi.com).

**About Teradyne**

Teradyne (NYSE:TER) is a leading supplier of Automatic Test Equipment used to test complex electronics used in the consumer electronics, automotive, computing, telecommunications, and aerospace and defense industries. In 2008, Teradyne had sales of \$1.1 billion and currently employs about 3,000 people worldwide. For more information, visit [www.teradyne.com](http://www.teradyne.com). Teradyne (R) is a registered trademark of Teradyne, Inc. in the U.S. and other countries. All product names are trademarks of Teradyne, Inc. (including its subsidiaries).

###

Editor's Note: For product photography and other resources, please visit Teradyne's press room at: <http://www.teradyne.com/pressRoom/index.html>

**For more information, contact:**

Jessica Faulkner  
*Public Relations*  
Tel 978.370.1437  
[jessica.faulkner@teradyne.com](mailto:jessica.faulkner@teradyne.com)

Bertrand Renaud  
*Public Relations*  
Tel: 781.891.1148  
[Bertrand.renaud@galaxysemi.com](mailto:Bertrand.renaud@galaxysemi.com)