



**For Immediate Release**

## **Verigy Selects Galaxy Examiner™ for Test Data Analysis**

**WALTHAM, Mass, - May 12, 2009** -- Galaxy Semiconductor Solutions, a leading provider of test data analysis software, today announced that Verigy, Ltd., a global leader in semiconductor test equipment, has selected Galaxy's Examiner software for use by its applications engineers to streamline the test program generation process, enhancing test correlation and quality.

Examiner™ is a software solution that enables test and product engineers to perform characterization, production monitoring, and yield analysis studies from standard semiconductor data files such as STDF, GDF and EDL. With just a single click Examiner™ generates comprehensive reports, such as: yield summary, Cp, Cpk, histograms, wafer maps, Pareto diagrams, and gage R/R. In addition, Examiner™ performs automatic guard-banding of test limits, and lets engineers perform "what-if" experiments to explore the impact of test program changes on yield and repeatability.

"Examiner will enable Verigy applications engineers to accelerate test program generation on the V93000 test platform," said Pascal Ronde, Vice President of Global Sales, Services and Support at Verigy. "Using the Examiner toolset on the V93000 is another example of Verigy's commitment to leverage innovative productivity enhancement solutions which facilitate a fast and smooth transition to high volume production for our customers."

"Verigy and Galaxy have worked closely over the past few years to the benefit of our mutual customers," said Bertrand Renaud, COO of Galaxy Semiconductor. "We are very pleased to extend our relationship to include Verigy's worldwide applications team, and to formalize a productive partnership."

### **About Verigy**

Verigy provides advanced semiconductor test systems and solutions used by leading companies worldwide in design validation, characterization, and high-volume manufacturing test. Verigy offers scalable platforms for a wide range of system-on-chip (SOC) test solutions, and memory test solutions for flash, DRAM including high-speed memories, as well as multi-chip packages (MCP). Advanced analysis tools accelerate design debug and yield ramp processes for Verigy's customers. Information about Verigy can be found at [www.verigy.com](http://www.verigy.com).

### **About Galaxy**

Galaxy Semiconductor Solutions is a leading provider of test data analysis, yield enhancement and PAT software for the semiconductor industry. Galaxy products are used by over 2100 users worldwide to help increase device yields, reduce defects and improve engineering productivity. Galaxy products are easy to learn and use, and offer a very short time-to-benefit, coupled with a compelling return on investment. More information about Galaxy can be found at [www.galaxysemi.com](http://www.galaxysemi.com).

CONTACT: Galaxy  
Bertrand Renaud, 781-891-1148  
[Bertrand.renaud@galaxysemi.com](mailto:Bertrand.renaud@galaxysemi.com)

SOURCE: Galaxy Semiconductor Solutions